A Novel Structure of Microstrip Coupled Bandstop Filter Based on Shorted Step-impedance Transmission Lines

P. Booppha\textsuperscript{a,\*}, R. Phromloungsri\textsuperscript{a}, S. Srisawat\textsuperscript{a}, N.Pornsuwancharoen\textsuperscript{b}

\textsuperscript{a}Department of Electronics Engineering, Faculty of Technology, Udonthani Rajabhat University, Udonthani, 41000, Thailand

\textsuperscript{b}Rajamangala University of Technology Isan Sakonnakhon Campus, Sakonnakhon 47160, Thailand

Abstract

This paper presents a novel structure of microstrip coupled bandstop filter based on shorted step-impedance transmission lines (SITL). The modified coupled lines with SITL have bandstop frequency response, which can be used for bandstop filter implementation. To verify the satisfaction between theory and experiment, a 1.8 GHz bandstop filter is fabricated on FR4 substrate. The measurement results show -31.85 dB insertion and -7.74 dB return loss.

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1. Introduction

Quadrature microstrip parallel-coupled lines are widely utilized for many wireless and microwave circuits such as Marchand balun, filters, resonators and various microwave integrated circuits because they can easily be incorporated into and implemented with other circuits [1],[2]. However, many unwanted effects in those circuits are already mentioned and unavoidably, which is resulted from the inhomogeneous dielectric of the substrate. Through this structure, phase velocity of the even-mode in microstrip parallel-coupled lines is slightly lower than the odd-mode phase velocity. Which in turn lead the parallel-coupled lines exhibits poor directivity. Through having various disadvantages, parallel-coupled lines is still preferable in microwave circuits design due to its integrating capability. Many techniques have been developed to compensate the inequality of modes phase velocities of the parallel couplers. These techniques can be classified into two main categories, which are lumped and distributed compensation approaches.

The lumped compensation techniques can be categorized into two well known techniques, which are capacitive [5],[6] and inductive [7],[8] compensation techniques. The distinct advantage of the lumped compensation technique is its simple design procedure because the closed-form design equations can be derived.

* Corresponding author. \textit{E-mail address:} icurvip@yahoo.com
But, the important disadvantages of these techniques are from the lumped components’ parasitic and difficulty in layout [9].

As the coupler sizes’ is considered, the lumped compensated parallel-coupled lines is about the uncompensated parallel-coupled lines, since the length compensated parallel-coupled lines is shorter than that of the uncompensated coupled lines. The methodology based on the distributed techniques is to modify either the parallel-coupled line structures [1],[2], dielectric layer [3], or ground plane patterns [4], such that the phase velocities of both modes are equalized. No external components or extra spaces are needed for this approach. The main disadvantage of this approach is lack of closed-form design equations, meaning that the design task relies heavily on the electromagnetic simulation (EM) stage which in turn consumes much effort and computing time. Moreover, techniques based on these approaches are often not suitable for some standard fabrication processes, thus more cost demand is unavoidably required.

Here we present a simple all microstrip step-impedance transmission line compensation technique to improve the directivity of parallel-coupled lines. Section II presents the idea of the singly- and doubly-step impedance resonators compensated parallel-coupled lines. The technique can be achieved high-directivity by connecting the SIRs in series with coupled and through ports of the parallel-coupled lines. Simulated and measured results of the proposed compensation couplers will be illustrated in Section III. The paper is finally concluded in Section IV.

2. The Proposed Techniques

The fundamental concept in this technique is to achieve high-isolation performance by the generation of isolation zero (S₁₃ = 0) between input and isolated ports of the parallel-coupled lines, which in turn results in a high directivity performance of the coupler. As the previous inductive compensation techniques [8],[9], which involves connecting external single or multiple inductive components series with the coupler’s ports. The techniques in this paper are achieved by applying step impedance transmission line to enhance the directivity and isolation of microstrip parallel-coupled lines. The proposed techniques are then, so called singly- and doubly-step impedance transmission line compensation technique.

A. Step Impedance Resonator

Generally, step impedance resonators (SIR) is a non-uniform transmission line, which were used in the filter design so as to reduce the circuit sizes [15], to push the spurious passband to the higher frequency, and even to suppress the multiple spurious passband [16]. The step impedance resonator employed in this paper was shown.
in Fig. 2, it is made of two transmission line sections with two different characteristic impedances $Z_a$ and $Z_b$ with corresponding electrical lengths $\theta_1$ and $\theta_2$, respectively. The input impedances looking in to section $b$, $Z_{in}(b)$ with load $Z_L = \gamma$ terminated at one end and the input impedance looking in to section $a$, $Z_{in}(a)$ with load impedance $Z_L = Z_{in}(b)$ can be written as

$$Z_{in}(b) = -jZ_b \frac{1}{\tan q_b}$$

(1)

$$Z_{in}(a) = jZ_a \frac{(Z_a \sin q_a \sin q_b - Z_b \cos q_a \cos q_b)}{(Z_a \sin q_a \cos q_a + Z_b \sin q_a \cos q_b)}$$

(2)

In the technique, $Z_a > Z_b$, so the impedance ratio $R_c = Z_b / Z_a$ is align between $0 \leq R_c \leq 1$. The parameters $Z_a$, $Z_b$ and $\theta_1$, $\theta_2$ should be selected to control transmission coefficient $S_{21}$ satisfied the condition $S_{21}(2f_0) = 0$.

$$Z_{ST1}(f_0) = -j \frac{(Z_o^2 Z_{in} \sin q_o - Z_o Z_{in} \sin q_o + 2Z_o Z_b + Z_o Z_a)}{Z_a + Z_b}$$

(3)

Where $Z_a = jZ_o (\sin q_o \cdot \sin q_o)$ and $Z_o = Z_o^2 (\cos q_o \cdot \cos q_o)$, $Z_{ST1}(f_0)$ is the singly-series compensation impedance which related to parallel-coupled lines’ electrical parameters and $\theta_e = \pi/2$ is the even-mode electrical length of the coupled lines, $\theta_o = (\pi/2)\Theta = \Theta$ is the odd-mode electrical length of the coupled lines and

$$\Theta = \sqrt{\epsilon_{eff} / \epsilon_{e}}.$$  

C. Doubly-Step Impedance Resonator Compensated Coupler

In this technique, two sections of SIR are series connected at coupled and through ports of parallel-coupled lines as shown in Fig. 1 (b). The isolation coefficient ($S_{31}$), in term of coupler electrical parameters ($Z_0, Z_{0e}, Z_{0o}, \epsilon_{e}, \epsilon_{o}$), is obtained by terminating port 2, 3, and 4 with the characteristic impedances of the lines $Z_0$, while port 2 is terminated with voltage source with the output impedance $Z_o$. The optimum directivity (about 20 dB) at center frequency $f_0$ is occurred when the signal transmission of port 1 and 3 is enforced to be nearly zero or $S_{31}(f_0) \simeq 0$, which will occurred as the input impedance of singly SIR section $Z_{in}(a)$ is also enforced to be

$$Z_{ST1}(f_o) = Z_{in}(a)$$

Where

$$Z_o = jZ_0 (\sin q_o \cdot \sin q_o)$$

and $Z_o = Z_o^2 (\cos q_o \cdot \cos q_o)$, $Z_{ST1}(f_0)$ is the singly-series compensation impedance which related to parallel-coupled lines’ electrical parameters and $\theta_e = \pi/2$ is the even-mode electrical length of the coupled lines, $\theta_o = (\pi/2)\Theta = \Theta$ is the odd-mode electrical length of the coupled lines and

$$\Theta = \sqrt{\epsilon_{eff} / \epsilon_{e}}.$$  

Fig.2. Schematic of the employed two-sections step impedance resonator (SIR)
frequency $f_0$ is occurred when the signal transmission of port 1 and 3 is null or $S_{31}(f) = 0$, its will occurred as the input impedance of doubly SIR sections $Z_w(a)$ are enforced to be

$$Z_{st2}(f_0) = \frac{j \left( Z_a^2 Z_{w0} \sin q_a - Z_b^2 Z_{w0} \sin q_b \right) - Z_w^2 Z_w - 2Z_w Z_b}{Z_b + Z_w Z_w}$$

$$= Z_w(a)$$

(4)

Where $Z_{st2}(f_0)$ are the doubly-series compensation impedances that related to parallel-coupled lines’ electrical parameters.

3. Design and Experimental Results

To validate the performance of developed techniques the design examples of 10-dB uncompensated and the proposed singly- and doubly- step impedance resonator compensated parallel-coupled lines designed at $f_0$ of 0.9 GHz on RF60 substrate ($\varepsilon_r = 6.0$, $h = 1.52$ mm, and $\tan d = 0.002$) were designed and fabricated. In the design, the experiment The SIR sections in both techniques were designed by using $Z_a = 100$ W and $Z_b = 70$ W, while $q_a$ and $q_b$ were achieved by optimization. The design parameters of prototype of uncompensated and the proposed couplers corresponded to equations (3), (4) were listed in Table I. The simulated results of uncompensated and the both proposed couplers were shown in Fig. 3 (a) and (b). The EM simulator Sonnet-Lite™ is used to simulate the couplers and meander lines inductors behavior. Fig. 4 shows EM simulated results of the proposed coupler. An excellent directivity and isolation improvement can be obviously observed.

![Simulated results of uncompensated (……..) the singly (----) and doubly-(———) SIR compensated coupler](image-url)
The EM simulated directivity and isolation at frequency $f_0$ are more than 43 dB and 50 dB, which is more than 25 dB improvement compared to the uncompensated coupler. In this work, all measurement is done by HP8753E vector network analyzer calibrated from 0.1 to 3.0 GHz. The HPVEE6.0™ software was used to collect the experimental data via GPIB card. Sonnet-Lite™ and Matlab® were used for simulation, data processing, and display.
The measured results of the proposed compensated singly- and doubly- SIR compensated compared with uncompensated couplers in Fig. 5. The directivity and isolation performances from the singly- and doubly-compensated techniques are 6 dB and 12 dB better, respectively, than those of the uncompensated coupled lines from 0.8 GHz to 1.0 GHz. At the $f_0$ of 0.9 GHz, the doubly-compensated design provides isolation and directivity performance approximately 25 dB better than the uncompensated coupled lines, while the singly-compensated design achieves around 7dB improvement. The photograph of uncompensated and the both singly- and doubly-SIR compensated couplers are shown in Fig. 6.

4. Conclusions

A singly-and doubly step impedance resonator compensated techniques are proposed to enhance the directivity of microstrip parallel-coupled lines. The techniques are very simple to design and fabricated, since its all microstrip structures, over 6 dB and 30 dB directivities enhancement for both proposed couplers as inductively-compensated techniques are achieved in our experiments. However, the complete closed form design equations will be developed in the future to simplify the design procedures. The proposed couplers are suitable to use in many wireless and microwave applications.

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References


Appendix A.

<table>
<thead>
<tr>
<th>Techniques</th>
<th>Lumped Components</th>
<th>Coupler’s Length (θ, rad)</th>
<th>W,S,L (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Uncomp.</td>
<td>-</td>
<td>0.50π</td>
<td>1.76, 0.38, 40.0</td>
</tr>
<tr>
<td>Singly-Inductive</td>
<td>$L_s = 2.38 \text{ nH}$</td>
<td>0.43π</td>
<td>1.76, 0.38, 35.0</td>
</tr>
<tr>
<td>Doubly-Inductive</td>
<td>$L_d = 2.51 \text{ nH}$</td>
<td>0.46π</td>
<td>1.76, 0.38, 35.0</td>
</tr>
<tr>
<td>Dydyk-Capacitive</td>
<td>$C_D = 0.447 \text{ pF}$</td>
<td>0.50π</td>
<td>1.76, 0.38, 40.0</td>
</tr>
<tr>
<td>March-Capacitive</td>
<td>$C_M = 0.454 \text{ pF}$</td>
<td>0.45π</td>
<td>1.76, 0.38, 35.0</td>
</tr>
</tbody>
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